

Simulation Study of High Performance Low Pass filter at Nanoscale

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Abstract: This paper presents a design and simulation study of high performance low-pass filter based on Cascode OTA using 0.9 V. The cascode OTA is used as the active building block. It is a new class of Operational Amplifier (OP-AMP). It has flexibility and tunability unlike Op Amp. Further, CNT technology is used along with MOS technology to design and simulate proposed structure at 45nm technology node. The proposed structure can be used. Figure 2 shows the schematic diagram of one of the important application of CNT, that is, Carbon Nanotube Field Effect Transistor (CNTFET). In a CNTFET, the channel is made up of parallel combination of SWCNTs. The source and drain regions are highly doped regions and the CNT channel is undoped. The important advantages of CNTFET include 1D ballistic transport of charge carriers, high mobility, large drive current and very low power consumption for low power applications. It is observed that the proposed LPF is also consuming low power of 28.43 μ W.

Keywords: CMOS, CNTFET, DC Gain, Cascode -OTA, Power consumption, Filters, LPF

I. INTRODUCTION

OTA is a building block whose differential input voltage produces an output current. It is also called as voltage controlled current source. There is usually an additional input for a current to control the amplifier's transconductance. The OTA is similar to a standard operational amplifier in that it has a high impedance differential input stage and that it may be used with negative feedback. Cascode OTA is a new class of OTA. It has improved gain (due to high output resistance) and bandwidth due to reduced Miller capacitance. In order to ensure further improvement in Cascode OTA performance, we have proposed Carbon Nanotube Field Effect Transistors (CNTFETs) based Circuits that promise to deliver much better performance than existing CMOS based Cascode Operational Transconductance Amplifiers. CNTFET technology can easily club with the bulk CMOS technology on a single chip and utilizes the same infrastructure at 45nm. An active low pass filter is an analog circuit that is widely used in communication systems and signal processing to pass a range of frequencies & reject the higher frequency. It can be easily designed by a conventional operational amplifier. But CMOS operational transconductance amplifier can be used to design a LPF resulting reduced power dissipation & fabrication cost [1-6].

II. CASCODE OPERATIONAL TRANSCONDUCTANCE AMPLIFIER (COTA)

The signal processing is well known that needs several functions for obtaining the desired signal and removing the unwanted signals. Analog filter is an important subsystem for achieving their functions. First of all, RLC circuits are preferred to use as a filter in the system but they had drawbacks if the band of frequency was changed. Manually tuned of capacitors and inductors was required but it is not practically used. Next generation of electronic design using large scale integrated circuit was exhibited. Versatile amplifier named OPAMP and RC were preferred to design as well as analog filter. Although, the manual tunability can be done but the completed system is quite large and cannot provided electronically controlled feature. OTA has more advantages compared to OP-AMP like higher input-output isolation, higher input impedance, high output impedance, higher gain or higher bandwidth with additional applications. Flexibility and tunability are the big advantages of OTAs. The output current i_o of the ideal OTA can be expressed by equation (1)

$$i_o = g_m (v_p - v_n) \quad (1)$$

Where g_m is the transconductance, v_p and v_n are positive and negative input terminals respectively. The ideal OTA has infinite output resistance. All of i_o flows in the external capacitive load and none flows in the OTA's own output resistance. Towards increasing the OTA output resistance, the current mirrors are cascoded. Cascode amplifier configuration improves gain due to high output resistance and bandwidth due to reduced Miller capacitance.

III. PROPOSED COTA BASED LOW PASS FILTER

The Cascode transconductance Operational transconductance (COTA) circuit is used to design LPF at 45nm. Figure Shows the Cascode OTA and proposed LPF is simulated using 0.9 V.

Figure 1 shows the schematic diagram of one of the important application of CNT, that is, carbon nanotube field effect transistor (CNTFET). In a CNTFET, the channel is made up of parallel combination of SWCNTs. The source and drain regions are highly doped regions and the CNT channel is undoped. The important advantages of CNTFET include 1D ballistic transport of charge carriers, high mobility, large drive current and very low power consumption [7-11].

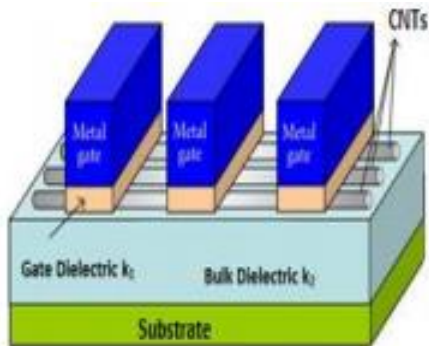


Figure 1. Schematic of a CNTFET

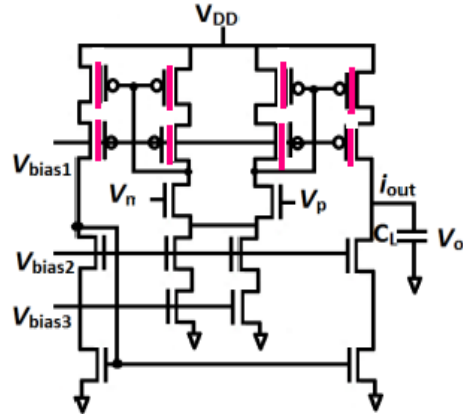


Figure 2. Cascode OTA

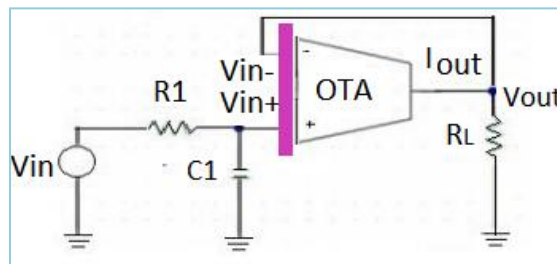


Figure 3. Proposed Low Pass Filter

Application of Cascode Operational Transconductance Amplifiers (COTAs) in Low Pass Filter. The active filter design using operational amplifier has a serious limitation over the applications in the high frequency regions.

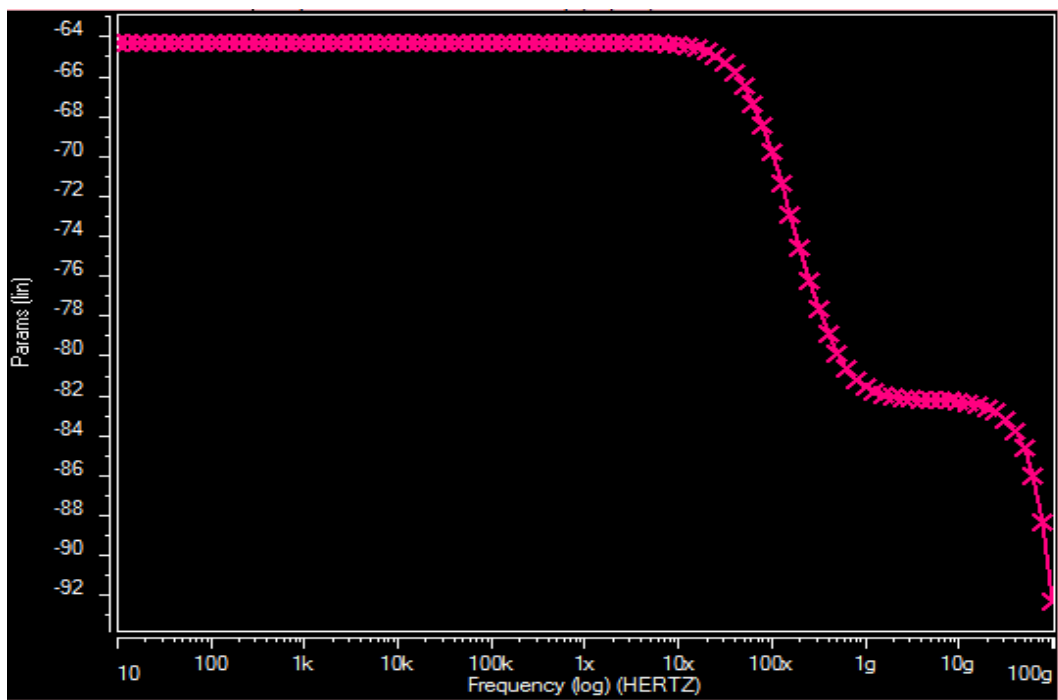


Figure 4. Frequency response of Proposed LPF



To overcome these limitations active filters using OTAs are popular due to the salient features of OTA such as , the adjustable transconductance (g_m) over wide range of bias current, excellent matching between amplifiers, the linearity of transconductance with bias current , controlled impedance buffers and high output signal to noise ratio, which popularizes OTA in active filter design. Filters can be readily built using COTAs. Considerable flexibility in controlling those specific filter characteristics that are usually of interest is possible with COTAs . In this paper, we have implemented Low pass filter.

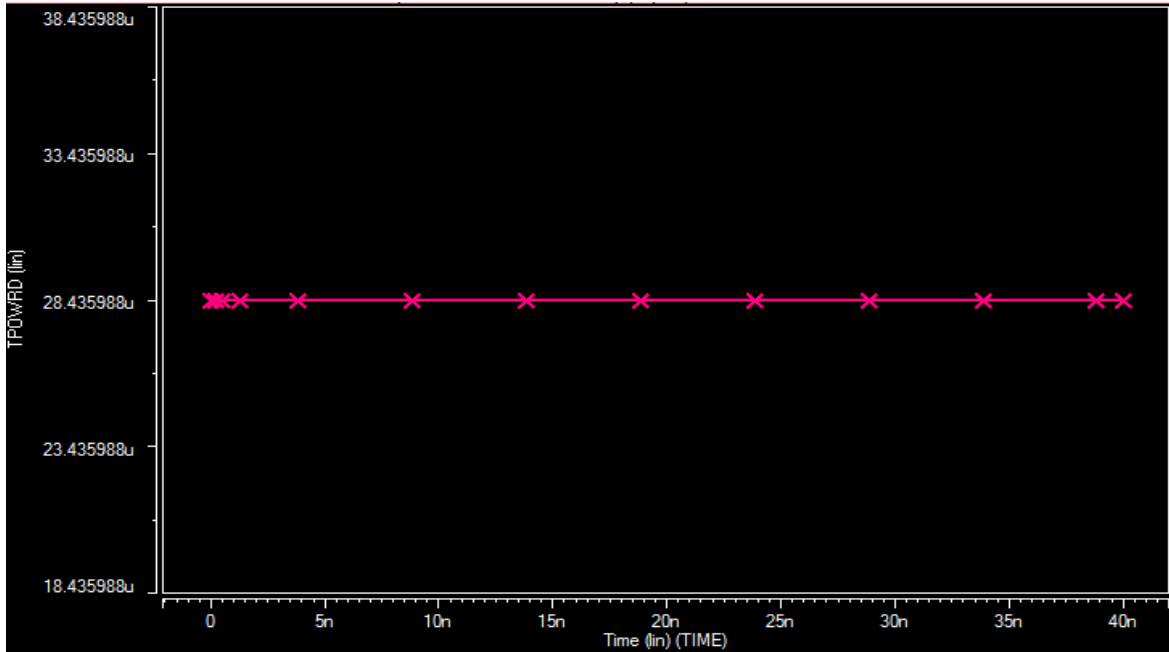


Figure 5. Average Power of Proposed LPF

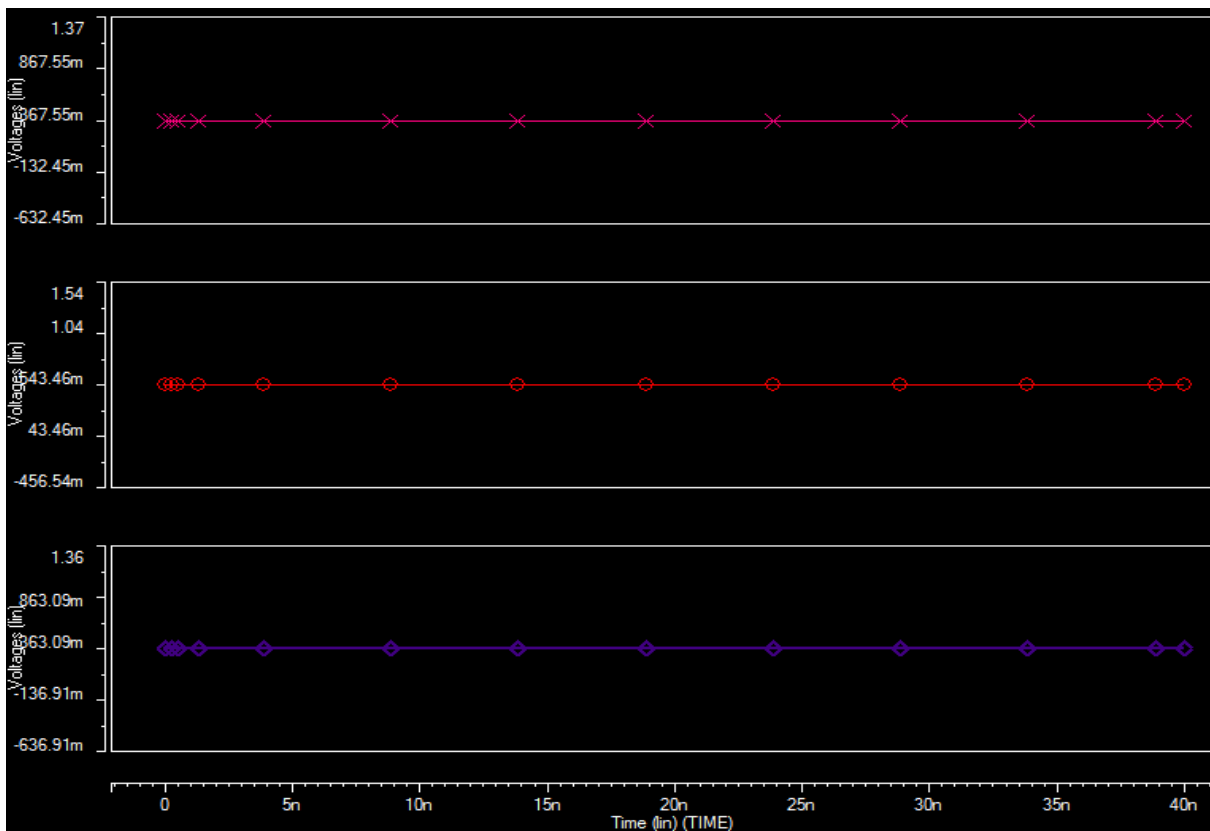


Figure 6. Biasing Supply used in Proposed LPF



IV. CONCLUSION

Cascode Operational Transconductance Amplifiers (COTA) based Low pass filter is designed and simulated at 45nm technology using 0.9V for analog applications. It has been designed and simulated using novel carbon nanotube based MOS structures and conventional MOSFETs. The proposed structure is useful in nanoelectronic circuits. It is observed that the proposed LPF is also consuming low power.

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