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AI-DRIVEN CHAOTIC SYSTEM FOR SECURE IMAGE ENCRYPTION AND DECRYPTION IN SIMULINK

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Abstract: An efficient ANN-based chaotic system for image encryption and decryption, implemented in Simulink and MATLAB using the Xilinx System Generator (XSG) tool. A feed-forward neural network (FFNN) with one hidden layer and Tangent Sigmoid activation ensures high accuracy, validated through a low mean square error. The generated Verilog code from XSG is executed in Vivado for pixel distribution, enabling hardware-level optimization. Security tests, including histogram analysis, PSNR, and SSIM, demonstrate robust encryption performance. The system's analysis and visualization, conducted within Simulink, MATLAB, and Vivado, confirm its effectiveness. This approach showcases ANN-based chaotic systems for secure and real-time image processing.

Keywords: Artificial Neural Networks (ANN), Chaotic Systems, Simulink, MATLAB.

I. INTRODUCTION

This project focuses on securing images using AI-driven techniques and chaotic systems for encryption and decryption, leveraging MATLAB's Simulink and Vivado tools. Chaotic systems generate unpredictable keys, ensuring highly secure encryption resistant to attacks. AI enhances the system by adding intelligence and adaptability.

Simulink enables efficient design, simulation, and testing of the encryption and decryption mechanisms, while Vivado facilitates seamless hardware synthesis and integration. The result is a robust system that protects sensitive images from unauthorized access.

II. METHODOLOGY

This diagram illustrates the methodology for a secure image encryption and decryption system using AI, chaotic systems, and tools like Simulink, Xilinx System Generator (XSG), and Vivado. The process begins with an input image undergoing pre-processing in MATLAB's Simulink to prepare it for encryption. Next, an artificial neural network (ANN)-based chaotic system is designed using XSG to generate secure and unpredictable encryption keys. The image is then encrypted and decrypted using ANNs to ensure security during transmission. Verilog code is generated using XSG and implemented in Vivado to optimize the encryption and decryption logic for pixel-level operations.

Finally, the decrypted image is interfaced with MATLAB for output, and the system's performance is evaluated using metrics such as histogram analysis, mean squared error (MSE), peak signal-to-noise ratio (PSNR), signal-to-noise ratio (SNR), and structural similarity index (SSIM). This comprehensive methodology ensures a robust and secure system for protecting sensitive images.

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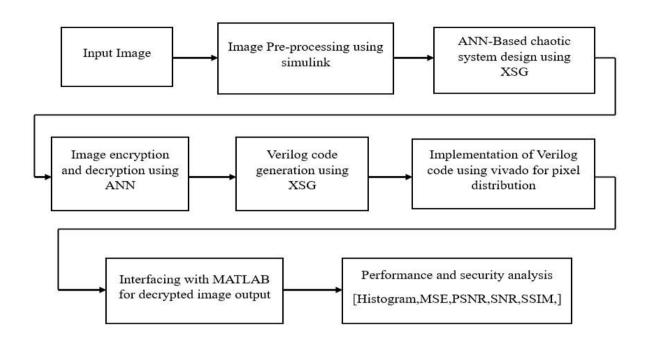
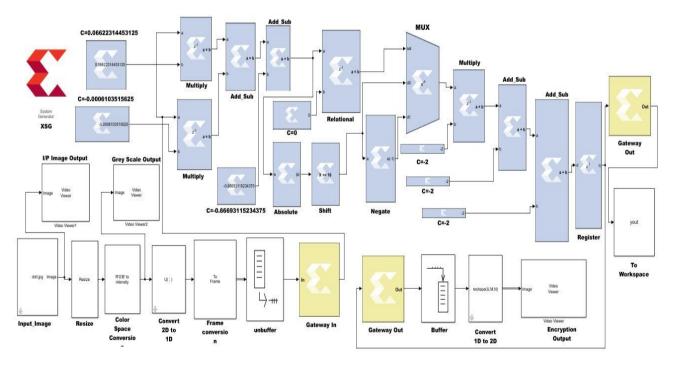


Figure1: Block diagram for Image Encryption and Decryption



III. IMPLEMENTATION

Figure 2: Block Diagram of Image Encryption System Using Xilinx System Generator (XSG) in Simulink

This diagram represents the implementation of an image encryption system using Xilinx System Generator (XSG) in MATLAB Simulink. The process begins with an input image, which is resized and converted from RGB to grayscale format. The 2D image is then flattened into a 1D vector for processing. Through a series of operations, including frame conversion and buffering, the data is passed through a gateway for further processing in the XSG environment.



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In the XSG section, chaotic systems are implemented using mathematical operations like multiplication, addition, subtraction, and shifting to generate encryption keys. These operations ensure high security by creating unpredictable outputs. The encrypted data is processed through multiple blocks for key integration and pixel manipulation, including relational comparisons, negations, and multiplexing (MUX). Finally, the encrypted image is reshaped back to its 2D form and displayed as the encryption output.

The system is designed to interact seamlessly with Simulink for pre-processing and visualization, while XSG handles the complex encryption logic. This implementation highlights an efficient integration of software tools and chaotic systems to achieve robust image encryption.

RESULT

IV.

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Running RGB:256x256 T=6.65e-06	Running 1:256x256 T=6.68e-06	Running 1:256x256 T=1.25e-05

Figure 3: Original, Grayscale, and Encrypted Image Output



Figure 4: Image and its Histogram Analysis.

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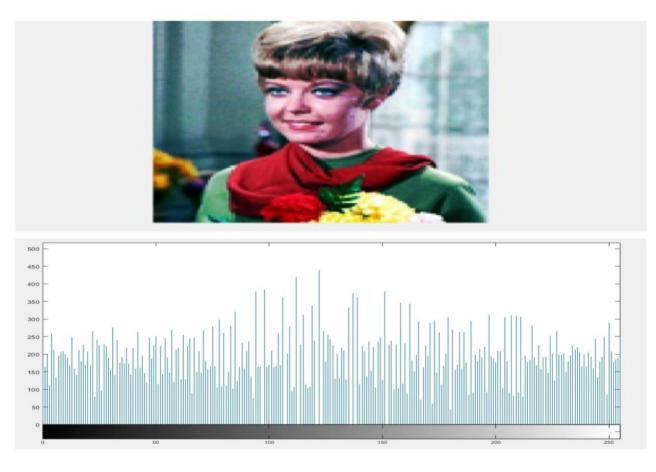
217

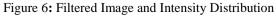


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堤 syn_en	1			
🔓 out_en1	1			
🔓 out_en2	1			
🔓 out_en3	1			
🔓 im_output_en	Z			
😻 im_input1[7:0]	00	00		
im_input2[7:0]	00	00		
😻 im_input3[7:0]	00			
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¼ reset_n	1			
W_file1[31:0]	0000002	00000002		
😻 w_file2[31:0]	0000004	00000004		
W file3[31·0]	0000008	00000000		

Figure 5: Signal Behavior During Encryption Process in Vivado





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>> final_m_code
Mean Squared Error (MSE): 1.6422
Peak Signal-to-Noise Ratio (PSNR): 45.9365 dB
Signal-to-Noise Ratio (SNR): 41.5623 dB
Structural Similarity Index (SSIM): 0.9047

Figure 7: Image Quality Metrics Report

V. CONCLUSION

This project presents an efficient image encryption and decryption system using AI-driven techniques, chaotic systems, and tools like MATLAB's Simulink and Xilinx System Generator (XSG). Chaotic systems generate secure keys, while AI enhances adaptability and security. Simulink supports seamless design and simulation, ensuring efficient implementation of the system.

Performance metrics such as histogram analysis, MSE, PSNR, SNR, and SSIM validate the system's reliability in safeguarding image integrity. By integrating software-based methodologies, this system ensures high security and scalability, demonstrating its potential for protecting sensitive images in real-world applications.

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